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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Frank Vanselow

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Docket:

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For:

Circuit Assembly for Generating a Phase-Locked Frequency-Modulatable

Carrier Frequency Signal

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TRANSMITTAL OF FORMAL DRAWINGS

JUL 2 3 2002

Technology Center 2600

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Continuous Continuous

Assistant Commissioner of Patents Washington, DC 20231

Attention: Official Drafts Person

William B. Kempler, Reg. No. 28,228

Date

Dear Sir:

Submitted herewith are \_\_1\_sheet of formal drawings.

JUL 3 0 2002

Charge any necessary fee to Deposit Account No. 20-0000 Texas Instruments Incorporated. This authorization is enclosed in triplicate.

Respectfully submitted,

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